

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"6,535,924".pn.	USPAT	OR	OFF	2009/09/16 08:56
L2	1	L1 and request\$	USPAT	OR	ON	2009/09/16 08:56
L3	1	L1 and facilitate	USPAT	OR	ON	2009/09/16 08:58
L4	1	L1 and line with illust\$	USPAT	OR	ON	2009/09/16 09:00
L5	1	L1 and erased	USPAT	OR	ON	2009/09/16 09:01
L6	1	L1 and time	USPAT	OR	ON	2009/09/16 09:07
L7	1	L1 and determin\$	USPAT	OR	ON	2009/09/16 09:09
L8	2	("5659684" "5550984").pn.	USPAT	OR	OFF	2009/09/16 09:14
L9	1	"5550984".pn.	USPAT	OR	OFF	2009/09/16 09:21
L10	0	9 and peripheral	USPAT	OR	OFF	2009/09/16 09:21
L11	28525	peripheral near controller	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/16 09:22
L12	125	11 with advantag\$	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/16 09:23
L13	88	12 and @ad<="20020621"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/16 09:23
L14	1	"5659684".pn.	USPAT	OR	OFF	2009/09/16 09:50
L15	1	14 and fpga	USPAT	OR	OFF	2009/09/16 09:50
L16	0	L1 and peripheral	USPAT	OR	ON	2009/09/16 10:00
L17	258	11 with fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/16 10:00
L18	100	17 and @ad<="20020621"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/16 10:00

S1	74	726/10.cor.	USPAT	OR	OFF	2009/09/15 14:34
S2	1	"20060155982".pn.	US-PGPUB	OR	OFF	2009/09/15 14:34
S3	135	christensen near carl	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:24
S4	34	S3 and router	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:24
S5	19	S4 and configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:24
S6	1545	johnson near charles	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:27
S7	30	S6 and router and configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:27
S8	15	S7 and card	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:27
S9	16	arbuckle near lynn near howard	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/09/15 15:28
S10	1	"6,535,924".pn.	USPAT	OR	OFF	2009/09/15 15:34
S11	1	S10 and program\$6	USPAT	OR	ON	2009/09/15 15:45
S12	1	S10 and control with memory	USPAT	OR	ON	2009/09/15 15:48
S13	0	S10 and controller	USPAT	OR	ON	2009/09/15 15:49
S14	1	S10 and processor	USPAT	OR	ON	2009/09/15 15:50
S15	1	S10 and line near card	USPAT	OR	ON	2009/09/15 15:54
S16	1	S10 and fabric near card	USPAT	OR	ON	2009/09/15 15:57
S17	1	S10 and instructions	USPAT	OR	ON	2009/09/15 16:00
S18	1	S10 and config\$	USPAT	OR	ON	2009/09/15 16:01
S19	0	S10 and fabric same ingress	USPAT	OR	ON	2009/09/15 16:16
S20	1	S10 and memory with configuration	USPAT	OR	ON	2009/09/15 16:22
S21	1	S10 and memory with control	USPAT	OR	ON	2009/09/15 16:23

9/ 16/ 09 10:21:20 AM

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